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Haratsch 1-4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Erich Franz Haratsch,
Harish Viswanathan
Case: 1-4
Serial No.: 09/326,785
Filing Date: June 4, 1999
Group: 2776
Examiner: Unassigned

Title: Method and Apparatus for Reducing the Computational Complexity and Relaxing the Critical Path of Reduced State Sequence Estimation (RSSE) Techniques

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231

Signature: *Kevin M. Mason* Date: July 17, 2001

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TRANSMITTAL LETTER

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Submitted herewith are the following documents relating to the above-identified patent application:

- (1) Supplemental Information Disclosure Statement with the PTO-1449 and cited reference; and
- (2) Statement Under 37 C.F.R. §3.73(b) with attachments.

Respectfully submitted,

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